

Application Serial No. 10/044,487 - Filed January 11, 2002

**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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FEB 04 2005

Application No.: 10/044,487

Filed: January 11, 2002

Inventor(s):

Bodo K. Parady

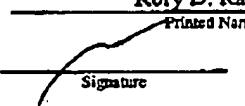
Title: Load Address Prediction
And New Thread
Identification In A
Multithreaded
Microprocessor

Examiner: Huisman, David J.
Group/Art Unit: 2183
Atty. Dkt. No: 5181-37601

I hereby certify that this correspondence is being sent via facsimile to: Facsimile No. 703-272-9306, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

Rory D. Rankin

Printed Name



February 4, 2005

Date

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Advisory Action of January 28, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

OK to Enter
DTM 2-4-05